

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

**Gerard Chauvel, et al.**

Serial No.: **10/631,308**

Filed: **07/31/2003**

For: **Mixed Stack-Based RISC Processor**

Docket No.: **TI-35433**

Examiner: **Petranek, Andrew J.**

Art Unit: **2183**

Conf. No.: **1887**

**INFORMATION DISCLOSURE STATEMENT**

April 1, 2008

Commissioner for Patents

P. O. Box 1450

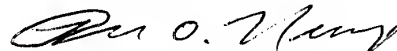
Alexandria, VA 22313-1450

Dear Sir:

In response to the Search Report received from the European Patent Office (a copy of which is enclosed), Applicant wishes to bring to the attention of the U.S. Patents and Trademarks Office the information noted on the enclosed PTO/SB/08A. The references were inadvertently omitted when the IDS was filed on 02/04/2008 and are therefore enclosed. All of these references were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to filing this statement.

The Commissioner is hereby authorized and requested to charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



/Ronald O. Neerings/

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